

Senior Design Logic

Responsibilities and Details

In this position, you will be a member of an Intel SOC design team based in Ireland working on leading edge CMOS designs. This silicon design team is involved in the design, validation and layout of leading edge Intel Architecture products. The software design team is involved in the design and validation of custom software for the embedded market.

The team is looking for engineers with strong RTL Design and Pre-Silicon Validation experience to augment the existing group and in addition to provide technical leadership. Your responsibilities will include but not be limited to:

- Developing the micro-architectural specification of complex design block(s)
- Responsible for the logic implementation of complex design block(s) using RTL coding techniques
- Working with pre-Silicon validation engineers to develop cluster level directed/random tests and environments
- Working with the Physical Design (Layout) team on Synthesis, Formal Verification and Timing Convergence

Qualifications

You should possess an honours degree, preferably a Master's degree and/or a Ph.D., in Engineering (Electronic Engineering) or equivalent. Additional qualifications include:

- Seven years of industry experience
- A strong background in RTL level Digital IC Design using System Verilog* and/or Verilog*
- Strong ASIC and/or SoC design experience
- Proven track record of successful first time delivery of projects
- A self starter with the ability to assume leadership roles
- Ability to work well in a diverse team environment
- Experience with industry standard development tools and methodologies
- Experience in unit level validation environment development, test plan generation and test case implementation for the verification of design block(s)
- The following qualifications would be added advantages:
- Experience with languages such as C/C++, SystemC, System Verilog*, Perl, Specman e, Shell scripting
- Experience in some of the following areas/tools: Synthesis, Formal Verification, DFT, VCS, PrimeTime, Design Compiler

Senior Design Layout Engineer

Description

In this position, you will be a member of an Intel SOC design team based in Ireland working on leading edge CMOS designs. This silicon design team is involved in the design, validation and layout of leading edge Intel Architecture products. The software design team is involved in the design and validation of custom software for the embedded market.

The team is looking for engineers with detailed knowledge on the complete layout lifecycle for complex SOC designs on the latest generation of Intel process technologies. Your responsibilities will include but not be limited to:

- Floorplan design for complex SOCs
- Working with DFX, RTL and validation engineers to influence micro-architectural approach to optimise final die
- Working with Mask and fabrication engineers to ensure smooth and rapid transfer of the design to manufacturing
- Working with tool providers to optimise the design flow

Qualifications

You should possess a high honours degree in Engineering (Electronic Engineering), Computer Science or other numerate discipline. Additional qualifications include:

- Seven years of industry experience
- Strong background in layout of digital designs
- Experience with Cadence*, Synopsys* and/or Mentor Graphics* tools
- Experience in a high speed Application-Specific Integrated Circuit (ASIC) environment
- Strong programming skills in languages such as C-shell, Perl, Scheme, tcl/Tk*, C and Lisp
- Proven track record of successful first time delivery of projects
- Ability to assume the leadership role
- Ability to work well in a diverse team environment
- Strong knowledge of CTS as applied in 90 and/or 65nm technologies would be an added advantage
- Experience with the latest generation of tools such as Jupiter*, Fict Encounter*, PKS* and Astro* would be an added advantage

Senior Silicon Validation Engineer

In this position, you will be a member of an Intel SOC design team based in Ireland working on leading edge CMOS designs. This silicon design team is involved in the design, validation and layout of leading edge Intel Architecture products. The software design team is involved in the design and validation of custom software for the embedded market.

The team is looking for engineers with strong Silicon Validation experience to augment the existing group and in addition to provide technical leadership. Your responsibilities will include but not be limited to:

- Developing comprehensive test strategies for right first time SOC design
- Understand the micro-architectural specification of complex design block(s) and be able to formulate test strategies to ensure that they are designed to very high quality
- Responsible for the test plan development for cluster and full-chip
- Working with Architect, DfX and RTL design engineers to develop cluster level directed/random tests and environments

Qualifications

You should possess a high honours degree in Engineering (Electronic Engineering), Computer Science or other numerate discipline. Additional qualifications include:

- Seven years of industry experience
- A strong background in RTL level validation and/or design using System Verilog* and/or Verilog*
- Experience in unit level validation environment development, test plan generation and test case implementation for the verification of design block(s)
- Strong ASIC and/or SoC design experience
- Experience with validation test environments such as OVM or VMM
- Proven track record of successful first time delivery of projects
- A self starter with the ability to assume leadership roles
- Ability to work well in a diverse team environment
- A self starter with the ability to assume the leadership role
- Ability to work well in a diverse team environment
- Experience with latest generation of verification and simulation tools on 90 and/or 65nm technologies would be an advantage

Senior Software Design Engineer

Description

In this position, you will be a member of an Intel SOC design team based in Ireland working on leading edge CMOS designs. This silicon design team is involved in the design, validation and layout of leading edge Intel Architecture products. The software design team is involved in the design and validation of custom software for the embedded market.

The team is looking for software engineers with detailed experience in the architecture and design of embedded software. Your responsibilities will include but not be limited to:

- Working directly with external customers and internal marketing teams to formulate successful software strategies to fully address customer requirements
- Work directly with hardware architects and logic designers to influence overall system design
- Developing software architecture strategies and specifications to optimise the final customer experience
- Supervise software engineers to implement specified software to a very high quality level

Qualifications

You should possess at least a Bachelor of Science degree and/or a Bachelor's degree majoring in Engineering. A higher degree is preferable. Additional qualifications include:

- Seven plus years experience in embedded software
- Implementation and/or design and/or architecture
- Deep knowledge of embedded development methods, tool chains and practices
- Architectural experience working on real-time embedded operating systems. Deep hands on experience with VxWorks and other RTOS
- Experience in directing the work of others
- Experience managing the technical interfaces between companies or organizations
- Highly developed verbal and written communications skills
- Strong business acumen and organizational savvy
- Deep knowledge of computer architectures especially Intel® architecture and system level configuration
- Strong analytic problem solving skills

Program Manager

Description

Reporting to Director of Engineering IPD shall plan and coordinate software road map for IPD working closely with engineering management and marketing to deliver a consistent set of software products for IPD. Experience as a software project manager is essential - experience in telecom/datacomms is an advantage.

The ability to work in a highly dynamic loosely defined environment while balancing the conflicting requirements of a diverse set of stakeholders is essential.

Attention to detail and precision in communication based on data is an essential skill. A background in software engineering methods, metrics and processes such as SEI CMM would be highly advantageous.

Some travel will be required to other IPD sites worldwide - duration typically 5-10 days.

Qualifications

Degree or higher degree in computer science, electrical engineering or other highly numerate discipline.